

Model Counting at CRIL

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Model Counting ($\#SAT$)

Input: CNF-formula F

Output: Number of satisfying assignments of F

- ▶ generic $\#P$ -hard counting problem
- ▶ applications, e.g.
 - ▶ probabilistic graphical models
 - ▶ stochastic planning

- ▶ state-of-the-art practical solvers for #SAT far slower than SAT-solvers

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$$\text{PH} \subseteq \text{P}^{\#\text{SAT}[1]}.$$

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⇒ extremely hard problem (theory and practice)

Interaction Between Variables: Primal Graphs

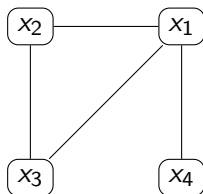
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- ▶ variables are vertices
- ▶ edge between variables that share clauses

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Example

- ▶ $(x_1 \vee x_2 \vee \neg x_3) \wedge (x_1 \vee x_4) \wedge (\neg x_2 \vee x_3) \wedge (x_1)$



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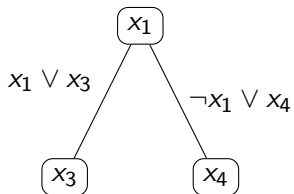
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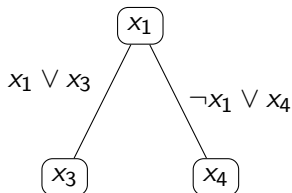
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- ▶ for every leaf v :

$$S(v, 0) = S(v, 1) = 1$$



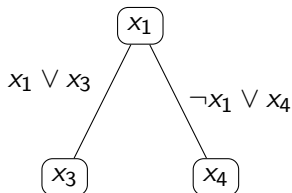
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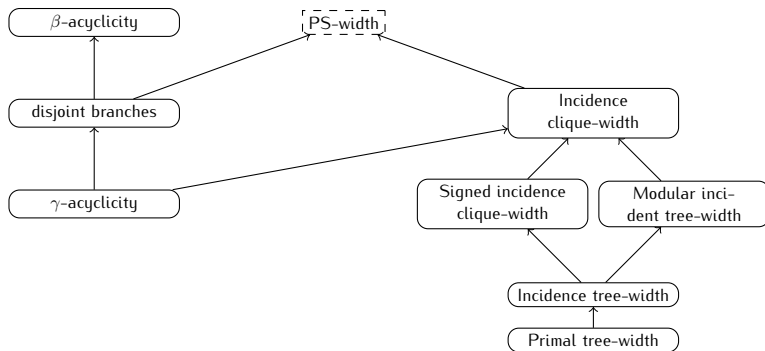
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also works in general

Which other graphs give tractable instances of $\#SAT$?

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Parameter of the hypergraph

Parameter of the incidence graph

► all algorithms dynamic programming

(Samer, Szeider '10; Paulusma, Slivovsky, Szeider '13; Fisher, Makowsky, Ravve '08; Slivovsky, Szeider '14; Capelli Durand, M '14; Brault-Baron, Capelli, M '15; Sæther, Telle, Vatshelle '14)

- ▶ runtimes for formula F
 - ▶ treewidth k FPT: $2^k |F|$
 - ▶ cliquewidth k not FPT: $|F|^k$
- ▶ FPT runtime far preferable

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Can runtime for cliquewidth be improved to FPT?

- ▶ runtimes for formula F
 - ▶ **treewidth** k FPT: $2^k |F|$
 - ▶ **cliquewidth** k not FPT: $|F|^k$
- ▶ FPT runtime far preferable

Question

Can runtime for cliquewidth be improved to FPT?

- ▶ NO, unless strange things happen, i.e. $\text{FPT} = \text{W}[1]$ (Paulusma, Slivovsky, Szeider '13)
- ▶ but can we say something unconditionally?

- ▶ Exhaustive DPLL
 - ▶ backtracking search to find/count solutions of CNF
 - ▶ caching (reuse counts of subformulas already solved)
 - ▶ components (independent formulas treated independently)
- ▶ basis of state of the art model counters

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Theorem (Huang, Darwiche '05)

Traces of runs of exhaustive DPLL are DNNF.

- ▶ DNNF
 - ▶ restricted boolean circuits
 - ▶ extensively studied in knowledge compilation
- ▶ so model counters can also compile: sharpSAT \rightsquigarrow Dsharp



Theorem (Bova, Capelli, M, Slivovsky '15, Capelli '16)

Consider any of the known graph based model counting algorithms.

Then its traces are also DNNF.

- ▶ consequence: essentially all known counting algorithms lead to DNNF (except linear algebra...)

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- ▶ consequence: essentially all known counting algorithms lead to DNNF (except linear algebra...)
- ▶ to show that known counting algorithms do not solve $\#SAT$, “only” need to show DNNF lower bounds
- ▶ ...but that was an open problem for 15 years

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Functions with high multipartition communication complexity have no small DNNF.

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Theorem (roughly, M '16)

*Cliqueswidth k formulas generally require DNNF size n^k
Thus known counting algorithms require time n^k .*

- ▶ model counting is hard
- ▶ graph restrictions help to a certain extent
- ▶ connection to compilation (DNNF)
- ▶ lower bounds with communication complexity
- ▶ cliquewidth harder than treewidth

- ▶ Many model counters (both exact and approximate) have been developed so far
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 - ▶ **compilation-based:** C2D, SDD, etc.

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- ▶ **Objective at CRIL:** Improving model counters by preprocessing the input (a CNF formula)

Off-line approaches for improving the model counting task

- ▶ knowledge compilation



[IJCAI'13 - joint work with F. Koriche, J.-M. Lagniez and S. Thomas]

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- ▶ **preprocessing**



[AAAI'14 - joint work with J.-M. Lagniez]

[IJCAI'16 - joint work with J.-M. Lagniez and E. Lonca]

Gate identification and replacement is a valuable preprocessing for model counting [AAAI'14 - joint work with J.-M. Lagniez]

$$\Sigma = \begin{array}{l} \bar{x} \vee u \vee v \\ \bar{x} \vee \bar{y} \vee u \\ \bar{x} \vee \bar{z} \vee u \\ x \vee \bar{u} \\ y \vee z \vee \bar{u} \end{array}$$

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$$(\bar{x} \vee u \vee v) \wedge (u \leftrightarrow (x \wedge (y \vee z))) \quad \text{identification}$$

$$\Sigma \equiv$$

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$$\|\Sigma\| = \|\Sigma[u \leftarrow (x \wedge (y \vee z))]\| = \|\bar{x} \vee y \vee z \vee v\| = 15$$

- ▶ The replacement phase **requires gates to be identified**
 - ▶ The search space for gates is **huge**: if $|\text{Var}(\Sigma)| = n$, then for each variable x of Σ , there exist

$$2^{2^{n-1}} \text{ gates}$$

with output x

- ▶ The size of a gate can be **huge** as well: not polynomially bounded in $|\Sigma| + |X|$ unless $\text{NP} \cap \text{coNP} \subseteq \text{P/poly}$

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- ▶ The size of a gate can be **huge** as well: not polynomially bounded in $|\Sigma| + |X|$ unless $\text{NP} \cap \text{coNP} \subseteq \text{P/poly}$
- ▶ Identifying "complex gates" is incompatible with the efficiency expected for a preprocessing:
only "simple" gates are targeted

literal equivalences $y \leftrightarrow x_1$

AND/OR gates $y \leftrightarrow (x_1 \wedge \bar{x}_2 \wedge x_3)$

XOR gates $y \leftrightarrow (x_1 \oplus \bar{x}_2)$

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- ▶ The replacement phase can be replaced by an **output variable elimination phase**
- ▶ There is **no need to identify** f to compute $\Sigma[y \leftarrow f(x_1, \dots, x_n)]$



- ▶ Σ explicitly defines y in terms of $X = \{x_1, \dots, x_n\}$ iff there exists a formula $f(x_1, \dots, x_n)$ over X such that

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- ▶ **Beth's theorem:** Σ **explicitly defines** y in terms of X iff Σ **implicitly defines** y in terms of X



Padoa's theorem:

Let Σ'_X be equal to Σ where each variable but those of X have been renamed in a uniform way

If $y \notin X$, then Σ (implicitly) defines y in terms of X iff $\Sigma \wedge \Sigma'_X \wedge y \wedge \overline{y'}$ is inconsistent



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Deciding whether Σ (implicitly) defines y in terms of X is "only" coNP-complete

The replacement phase can be replaced by an **output variable elimination phase**: if $y \leftrightarrow f(x_1, \dots, x_n)$ is a gate of Σ , then

$$\Sigma[y \leftarrow f(x_1, \dots, x_n)] \equiv \exists y. \Sigma$$

A two-step preprocessing

- ▶ "Identification = Bipartition":
compute a **definability bipartition** $\langle I, O \rangle$ of Σ such that $I \cup O = \text{Var}(\Sigma)$, $I \cap O = \emptyset$, and Σ defines every variable $o \in O$ in terms of I

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- ▶ "Replacement = Elimination":
compute $\exists E.\Sigma$ for $E \subseteq O$

Identification:

$\Sigma \wedge \Sigma'_{\{x,y,z\}} \wedge u \wedge \bar{u}'$ is inconsistent

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$$y \vee z \vee \bar{u}$$

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$$u$$

$$\bar{u}'$$

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Elimination:

computing resolvents over u

$$\overline{x} \vee v \vee x \quad \text{valid}$$

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$$\|\Sigma\| = \|\bar{x} \vee v \vee y \vee z\| = 15$$

Both steps **B** and **E** of **B + E** can be tuned in order to keep the preprocessing phase **light from a computational standpoint**

- ▶ It is not necessary to determine a definability bipartition $\langle I, O \rangle$ with $|I|$ minimal
 - ⇒ **B** is a **greedy algorithm** (one definability test per variable)
 - ⇒ Only the minimality of I for \subseteq is guaranteed

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 - ⇒ **B** is a **greedy algorithm** (one definability test per variable)
 - ⇒ Only the minimality of I for \subseteq is guaranteed
- ▶ It is not necessary to eliminate in Σ every variable of O but focusing on a subset $E \subseteq O$ is enough
 - ⇒ Eliminating every output variable could lead to an **exponential blow up**
 - ⇒ The elimination of $y \in O$ is committed only if $|\Sigma|$ after the elimination step and some additional preprocessing (occurrence simplification and vivification) remains **small enough**

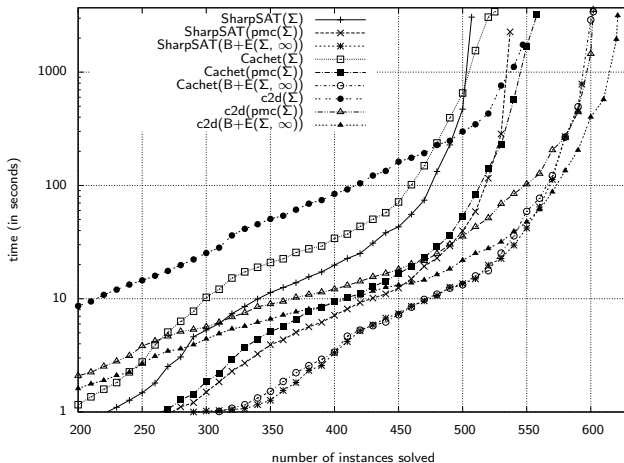
Objectives:

- ▶ Evaluating the computational benefits offered by B + E when used upstream to state-of-the-art model counters:
 - ▶ the search-based model counter Cachet
 - ▶ the search-based model counter SharpSAT
 - ▶ the compilation-based model counter C2D (used with `-count -in_memory -smooth_all`)

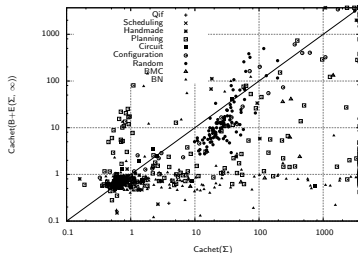
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 - ▶ the search-based model counter SharpSAT
 - ▶ the compilation-based model counter C2D (used with `-count -in_memory -smooth_all`)
- ▶ Comparing the benefits offered by $B + E$ with those offered by our previous preprocessor `pmc` (based on gate identification and replacement) or with no preprocessing

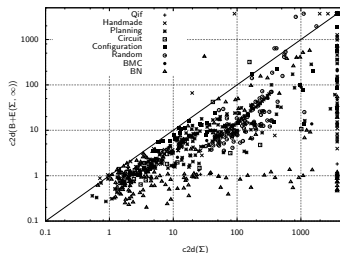
- ▶ 703 CNF instances from the SAT LIBrary
- ▶ 8 data sets: BN (Bayesian networks) (192), BMC (Bounded Model Checking) (18), Circuit (41), Configuration (35), Handmade (58), Planning (248), Random (104), Qif (7) (Quantitative Information Flow analysis - security)
- ▶ Cluster of Intel Xeon E5-2643 (3.30 GHz) processors with 32 GiB RAM on Linux CentOS
- ▶ Time-out = 1h
- ▶ Memory-out = 7.6 GiB



B + E vs. no preprocessing

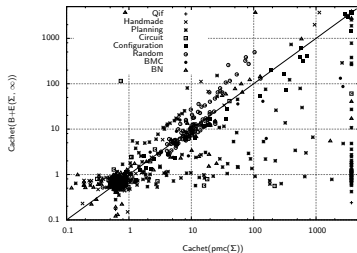


(a) B + E+Cachet vs. Cachet

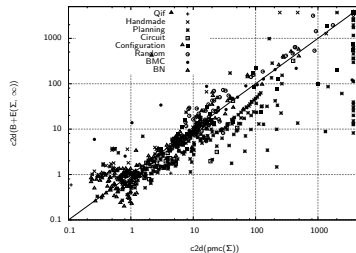


(b) B + E+C2D vs. C2D

B + E vs. pmc



(c) B + E+Cachet vs. pmc+Cachet



(d) B + E+C2D vs. pmc+C2D

Conclusion

- ▶ Design and implementation of the B + E preprocessor
- ▶ Empirical evaluation of B + E: for several model counters mc , $mc(B + E(.))$ proves computationally more efficient than $mc(.)$

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Perspectives

- ▶ Developing other ordering heuristics for B
- ▶ Connection to **projected model counting**: computing $\|\exists E.\Sigma\|$ given a set E of variables and a formula Σ
 - ▶ compute $\|\Sigma\|$ as $\exists mc(\Sigma, O)$ where $B(\Sigma) = \langle I, O \rangle$
 - ▶ compute $\exists mc(\Sigma, E)$ as $mc(E(\Sigma))$