

#### Incremental SAT and applications

Texte

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#### Introduction and Motivations

# SAT solving in practice

- SAT is a success story in computer science
- Many practical applications

Binate Covering Pedigree Consistency Function Decomposition Maximum SatisfiabilityConfigurationTermination Analysis Binate Covering Network Security Management Fault Localization Software Testing Filter Design Switching Network Verification Satisfiability Modulo Theories Package Management Symbolic Trajectory Evaluation Software Model Checking Constraint Programming **FPGA** Routing Timetabling Haplotyping Model Finding Ha Test Pattern Generation Planning **Logic Synthesis Design Debugging** Power Estimation Circuit Delay Computation **Genome Rearrangement** Lazy Clause Generation Pseudo-Boolean Formulas

Source J. Marques-Silva

### Erdos discrepancy conjecture (1932)

- Infinite sequence of +1 and -1 :  $< -1, 1, 1, -1 1, x_6, x_7, \ldots >$
- $\forall C \exists k, d \text{ such that } |\sum_{i=1}^{k} x_{i \times d}| \geq C$

- A new application
- The conjecture was proven for C = 2 using a SAT solver with an UNSAT certificate (Glucose...)
  - UNSAT proof... 13 Gb

cgi.csc.liv.ac.uk/~konev/SAT14/

www.newscientist.com/article/dn25068-wikipediasize-maths-proof-too-big-for-humans-to-check.html

## Seminal papers

"Planning as Satisfiability". Kautz and Selman. ECAI 92

 "Symbolic Model Checking Without BDD". Biere, Cimatti, Clarke and Zhu. TACAS 1999.



- "An extensible SAT solver". Een and Sorensson. SAT 2003
- Minisat source code





#### Preprocessing

- Encoding does not take into account connexions between some clauses and some variables
- Simplify the initial formula
- Very important



Decision variable heuristic

- Dynamic : Favor variables that are used recently in conflict analysis
- Phase saving (do not forget the past)



#### Restarts : dynamic or not



- Learnt clauses database cleaning
  - Avoid memory explosion and more

$$\begin{array}{l} C_1 = x_1 \lor x_4 \\ C_2 = x_1 \lor \overline{x_3} \lor \overline{x_8} \\ C_3 = x_1 \lor x_8 \lor x_{12} \\ C_4 = x_2 \lor x_{11} \\ C_5 = \overline{x_3} \lor \overline{x_7} \lor x_{13} \\ C_6 = \overline{x_3} \lor \overline{x_7} \lor \overline{x_{13}} \lor x_9 \\ C_7 = x_8 \lor \overline{x_7} \lor \overline{x_9} \end{array}$$

DL 1

# A short overview of CDCL solvers

#### Sequence of decision, propagations

 $C_{1} = X_{1} \lor X_{4}$   $C_{2} = X_{1} \lor \overline{X_{3}} \lor \overline{X_{8}}$   $C_{3} = X_{1} \lor X_{8} \lor X_{12}$   $C_{4} = X_{2} \lor X_{11}$   $C_{5} = \overline{X_{3}} \lor \overline{X_{7}} \lor X_{13}$   $C_{6} = \overline{X_{3}} \lor \overline{X_{7}} \lor \overline{X_{13}} \lor X_{9}$   $C_{7} = X_{8} \lor \overline{X_{7}} \lor \overline{X_{9}}$ 

DL 1

 $\overline{X_1}, X_4[C_1]$ 

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#### **Conflict analysis**

```
C_{1} = X_{1} \lor X_{4}
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#### **Conflict analysis**

 $C_1 = X_1 \lor X_4$   $C_2 = X_1 \lor \overline{X_3} \lor \overline{X_8}$   $C_3 = X_1 \lor X_8 \lor X_{12}$   $C_4 = X_2 \lor X_{11}$   $C_5 = \overline{X_3} \lor \overline{X_7} \lor X_{13}$   $C_6 = \overline{X_3} \lor \overline{X_7} \lor \overline{X_{13}} \lor X_9$   $C_7 = X_8 \lor \overline{X_7} \lor \overline{X_9}$ 



- First resolvant that contains only one literal of the last decision level
- First UIP learning scheme (related to implication graph)
- d<sub>1</sub> is added to the formula and...









### A word on Glucose

- SAT solver developed by Laurent Simon (LABRI) and myself
- Essentially based on a static measure identifying good learnt clauses (LBD)
  - Agressive cleaning strategy : bad clauses have big LBD value
  - Dynamic restarts based on LBD
  - Other features...

Efficient on UNSAT problems



## Incremental SAT solving : an introduction

A surprising effect of solvers' efficiency : used as NP-Complete oracles

- IC3 : thousands of calls of simple formulas [Bradley 2012]
- MUS extraction [Belov etal. 2012]
- ...
- Many calls on similar instances

CDCL solvers learnt form the PAST !!

#### Keep the solver alive

# Introduction to BMC

- Bounded Model Checking
- Verification of properties
- Given an automaton with transition states T, an initial state I and a given property P that must be true at each step
- Bound the number of steps (by k), check if P is falsified
- General form :  $I(s_0) \land T(s_0, s_1) \land T(s_1, s_2) \land \ldots \land T(s_{k-1}, s_k) \land \overline{p(s_k)}$
- Instance SAT : a bug is found
- Instance UNSAT : We know .... nothing, we need to increase the bound k







Step			
a b e d			







Step	0	$t_{3}^{0}$	
a b e d	F F T		



Step	0	$t_{3}^{0}$	1		
a b e d	F F T		F F T F		



Step	0	$t_{3}^{0}$	1	$t_{4}^{1}$	2	
a b e d	F F T		F F T F		F T F F	

MUS



Step	0	$t_{3}^{0}$	1	$t_{4}^{1}$	2	t <sub>1</sub> <sup>2</sup>	3
a b e	F F F		F F T		F T F		T F F
## Example : Resulting clauses



Initial State : <sup>d</sup> 0
Only one state at each step :
Only one transition at each state :
Transition can occur only if one is on the departure state : $(\neg d_0 \lor t_3^0) \land (\neg c_O \lor t_4^0) \ldots$ $(\neg d_1 \lor t_3^1) \land (\neg c_1 \lor t_4^1) \ldots$ 
After a transition, one is on the arrival state : $(\neg t_3^0 \lor c_1) \land (\neg t_4^0 \lor b_1) \ldots$ $(\neg t_3^1 \lor c_2) \land (\neg t_4^1 \lor b_2) \ldots$

 $I(s_0) \wedge T(s_0, s_1) \wedge (\neg p(s_1))$ 

 $\blacksquare l(s_0) \land T(s_0, s_1) \land (\neg p(s_1))$ 

$$\blacksquare I(s_0) \land T(s_0, s_1) \land T(s_1, s_2) \land (\neg p(s_1) \lor \neg p(s_2))$$

- $\blacksquare l(s_0) \land T(s_0, s_1) \land (\neg p(s_1))$
- $\blacksquare \ l(s_0) \land T(s_0, s_1) \land T(s_1, s_2) \land (\neg p(s_1) \lor \neg p(s_2))$
- $\blacksquare I(s_0) \land T(s_0, s_1) \land T(s_1, s_2) \land T(s_2, s_3) \land (\neg p(s_1) \lor \neg p(s_2) \lor \neg p(s_3))$





- Iterate on the same formula
- We need to add variables (very easy)
- We need to add clauses (very easy)
- We need to remove clauses (harder, see later...)

### Incremental SAT : another example

- Verification of properties on some circuit
- Too large too be fully encoded in SAT
- Need to select a sub-circuit and make BMC on it
- Use assumptions
- Mimic the environment of the larger circuit by imposing assumptions
- If the result is satisfiable
  - The environment is not set correctly, that is, assumptions are incorrect or missing
  - Or, there is a real bug

- A formula F
- A set of assumptions,  $\ell_1, \ell_2, \ldots, \ell_n$  with  $\ell_i$  are literals
- Solve  $F \wedge \ell_1 \wedge \ell_2 \dots \wedge \ell_n$
- Incremental SAT solving : the process can be repeated with new assumptions

- A formula F
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- Solve  $F \wedge \ell_1 \wedge \ell_2 \dots \wedge \ell_n$
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#### **First solution**

Simplify : 
$$F' = F \wedge \ell_1 \wedge \ell_2 \ldots \wedge \ell_n$$

Solve F'

Learnt clauses can not be kept

- A formula F
- A set of assumptions,  $\ell_1, \ell_2, \ldots, \ell_n$  with  $\ell_i$  are literals
- Solve  $F \wedge \ell_1 \wedge \ell_2 \dots \wedge \ell_n$
- Incremental SAT solving : the process can be repeated with new assumptions

- A formula F
- A set of assumptions,  $\ell_1, \ell_2, \ldots, \ell_n$  with  $\ell_i$  are literals
- Solve  $F \wedge \ell_1 \wedge \ell_2 \dots \wedge \ell_n$
- Incremental SAT solving : the process can be repeated with new assumptions

#### **Second Solution**

- First, selects all assumptions as decision variables : one level => one assumption
- Second, Run the SAT solver as usual
- All learnt clauses can be kept

One can explain unsatisfiability wrt set of assumptions !



• A set of assumptions,  $\ell_1, \ell_2, \ldots, \ell_n$  with  $\ell_i$  are literals



- If  $\neg \ell_i$  must be assigned, UNSAT must be returned
- Analysis of the reason of  $\neg \ell_i$  gives the subset of assumptions responsible of the conflict







$x \lor y \lor z$	$x \vee \neg y$	$X \vee \neg Z$
$\neg x \lor y \lor z$	$X \vee W$	$w \lor z \lor \neg y$
$\neg x \lor \neg y$	$\neg X \lor \neg Z$	$W \vee \neg X \vee \neg Z$
	UNSAT	



$x \lor y \lor z$	$x \lor \neg y$	$X \vee \neg Z$
$\neg x \lor y \lor z$	$x \lor w$	$w \lor z \lor \neg y$
$\neg x \lor \neg y$	$\neg X \lor \neg Z$	$W \vee \neg X \vee \neg Z$



- $\neg X \lor \neg Y \qquad \neg X \lor \neg Z \qquad W \lor \neg X \lor \neg Z$

- The formula is inconsistant : Why ?
- Minimal unsatisfiable subset of clauses
- Different approaches
  - Local search [Piette et al, ECAI 2006]
  - Resolution based [Nadel, FMCAD 2010]
  - Constructive or destructive [Belov etal, AI Com 2012]. The tool MUSER

	$x \vee \neg y$	$X \vee \neg Z$
$\neg x \lor y \lor z$	$X \lor W$	$w \lor z \lor \neg y$
$\neg x \lor \neg y$	$\neg X \lor \neg Z$	$W \vee \neg X \vee \neg Z$
	SAT	

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$\neg x \lor y \lor z$		$w \lor z \lor \neg y$
$\neg x \lor \neg y$	$\neg X \lor \neg Z$	$W \vee \neg X \vee \neg Z$
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$\neg x \lor y \lor z$		
$\neg x \lor \neg y$	$\neg X \lor \neg Z$	$W \vee \neg X \vee \neg Z$
	UNSAT	



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## **Muser Architecture**

#### **Incremental SAT**



- Successive calls to a SAT oracle
- Non independent calls
- Informations between two calls are preserved
  - Heuristics : VSIDS, phase saving, restarts...
  - Learnt clauses

Add one selector (fresh variable)  $\ell_i$  per clause



Learnt clause contains selectors of all original clauses used to generate it

- Assign  $\ell_i$  (as an assumption) to false to activate the clause *i*
- Assign  $\ell_j$  (as an assumption) to true to disable the clause j
- All learnt clauses related to the clause *j* a disable clause are disabled too !

$$l_{1} \lor x \lor y \lor z$$

$$l_{2} \lor x \lor \neg y$$

$$l_{3} \lor x \lor \neg z$$

$$l_{4} \lor \neg x \lor y \lor z$$

$$l_{5} \lor x \lor w$$

$$l_{6} \lor w \lor z \lor \neg y$$

$$l_{7} \lor \neg x \lor \neg y$$

$$l_{8} \lor \neg x \lor \neg z$$

$$l_{9} \lor w \lor \neg x \lor \neg z$$

$$\ell_1 \vee \ell_2 \vee x \vee z$$

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### Glucose inside Muser





#### Glucose inside Muser



Plug GLUCOSE in MUSER

Adapt and modify GLUCOSE to improve MUSER performances

#### Improve SAT oracle in order to improve the MUSER tool

Incremental SAT

Montpellier - March 2014





timeout set to 2400 seconds

MUSER is used with default options (destructive approach, model rotation)

# A first Attempt



# **Disappointing results**

Trying to explain these bad results


#### Trying to explain these bad results

- Comparable number of oracle calls
- Easy SAT calls
- Difficult UNSAT ones
- GLUCOSE is supposed to be good on UNSAT formulas

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- GLUCOSE uses LBD for cleaning, restarts...
- Each assumption uses its own decision level

- Each point represents an instance
- x-axis is the average number of initial variables in learnt clauses
- y-axis is the average number of selector variables in learnt clauses



Incremental SAT

		LBD								
			s	LE	LBD					
Instance	#C	time	avg	max	avg	max				
fdmus_b21_96	8541	29	1145	5980	1095	5945				
longmult6	8853	46	694	3104	672	3013				
dump_vc950	360419	110	522	36309	498	35873				
g7n	70492	190	1098	16338	1049	16268				

LBD looks like size

Clauses are very long

#### Trying to explain these bad results

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- The LBD of a clause looks like its size !

#### Trying to explain these bad results

- Comparable number of oracle calls
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- GLUCOSE is supposed to be good on UNSAT formulas

- GLUCOSE uses LBD for cleaning, restarts...
- Each assumption uses its own decision level
- The LBD of a clause looks like its size !

#### Refine LBD : Do not take into account selectors

# A second attempt



## New LBD

				LBD	)			1	New LB	D		
			size		L	LBD		size		LE	LBD	
Instance	#C	time	avg	max	avg	max		time	avg	max	avg	max
fdmus_b21_96	8541	29	1145	5980	1095	5945		11	972	6391	8	71
longmult6	8853	46	694	3104	672	3013		14	627	2997	11	61
dump_vc950	360419	110	522	36309	498	35873		67	1048	36491	8	307
g7n	70492	190	1098	16338	1049	16268		75	1729	17840	27	160

LBD matters

However, results need to be improve yet

Many algorithms have to traverse clauses



Conflict analysis

Unit propagation

Deleting satisfiable clauses

Many algorithms have to traverse clauses

- Dynamic computing of LBD (useful but costly)
  - → Store the number of selectors in the clause
  - → Stop when all initial literals have been tested
- Conflict analysis
- Unit propagation

Deleting satisfiable clauses

#### Many algorithms have to traverse clauses

#### Dynamic computing of LBD (useful but costly)

- Store the number of selectors in the clause
- → Stop when all initial literals have been tested
- Conflict analysis
  - → Force initial literals to be placed at the beginning
- Unit propagation

Deleting satisfiable clauses

#### Many algorithms have to traverse clauses

#### Dynamic computing of LBD (useful but costly)

- → Store the number of selectors in the clause
- → Stop when all initial literals have been tested

#### Conflict analysis

- → Force initial literals to be placed at the beginning
- Unit propagation
  - → Look for a non selector literal or a satisfied one
  - → Push selectors at the end of the clause
  - Deleting satisfiable clauses

#### Many algorithms have to traverse clauses

#### Dynamic computing of LBD (useful but costly)

- → Store the number of selectors in the clause
- → Stop when all initial literals have been tested

#### Conflict analysis

→ Force initial literals to be placed at the beginning

#### Unit propagation

- → Look for a non selector literal or a satisfied one
- → Push selectors at the end of the clause

#### Deleting satisfiable clauses

→ Take only watched literals into account

# Third attempt





# **Final comparison**



Incremental SAT





#### Application to knowledge compilation

# Autonomous Systems

- Autonomous system : must be able to make decisions depending on the current situation
- Finding the decision policy
  - Problem hard to solve

#### **Explorer Robot**

- Explore a zone
- Gather informations
- When enough informations have been gathered, go to another zone

#### Slides from Alexandre Niveau's PhD defense

# Control of an Autonomous Systems

#### Solving the problem online

- Limited by the embedded computational power
  - Reactivity not ensured

#### Solving the problem offline

- Anticipate all decisions for every possible situation
- Reactivity is ensured
- Limited by embedded memory ressources

Slides from Alexandre Niveau's PhD defense



# Looking for a Compromise

- Need a tradeoff between reactivity and spatial compactness
- Maximizing reactivity under memory space constraints
- This is the object of knowledge compilation

#### Idea

- Transform the problem into a compiled form
  - Make its resolution tractable
  - Is as compact as possible

Slides from Alexandre Niveau's PhD defense



# Two phases

#### Offline phase

- Theory is compiled into a target language
- Can be slow
- Need to be compact
- Some target languages : (RO)BDD, Tree of BD, DNNF, DNNF, SDD

#### Online phase

- The compiled target is used to efficiently answer a number of queries
- Need to be fast (polynomial ? ?)

MUS

### Some Queries

- Consistency ( $F \models \bot$ ?)
- Clause entailment ( $F \models c$ ?)
- Implicant ( $t \models F$ ?)
- Equivalence ( $F \equiv G$ ?)
- Sentential Entailment ( $F \models G$ ?)
- Model Counting
  - Model Enumeration

# Some Operators

- Conditioning : subset of variables is set to true/false
- Forgetting : Some variables are removed
- Conjunction :  $F \land G$
- Disjunction  $F \lor G$
- Negation ¬F



## Knowledge Compilation Map

 "A Knowledge Compilation Map". Adnan Darwiche, Pierre Marquis. J. Artif. Intell. Res. (JAIR) 17 : 229-264 (2002)

L	CO	VA	CE	IM	$\mathbf{EQ}$	SE	CT	ME
NNF	0	0	0	0	0	0	0	0
DNNF	$\checkmark$	0	$\checkmark$	0	0	0	0	$\checkmark$
d-NNF	0	0	0	0	0	0	0	0
s-NNF	0	0	0	0	0	0	0	0
f-NNF	0	0	0	0	0	0	0	0
d-DNNF	$\checkmark$	$\checkmark$	$\checkmark$	$\checkmark$	?	0	$\checkmark$	$\checkmark$
sd-DNNF	$\checkmark$	$\checkmark$	$\checkmark$	$\checkmark$	?	0	$\checkmark$	$\checkmark$
BDD	0	0	0	0	0	0	0	0
FBDD	√	$\checkmark$	$\checkmark$	$\checkmark$	?	0	$\checkmark$	$\checkmark$
OBDD	$\checkmark$	$\checkmark$	$\checkmark$	$\checkmark$	$\checkmark$	0	$\checkmark$	$\checkmark$
OBDD<	$\checkmark$	$\checkmark$	$\checkmark$	$\checkmark$	$\checkmark$	$\checkmark$	$\checkmark$	$\checkmark$
DNF	$\checkmark$	0	$\checkmark$	0	0	0	0	$\checkmark$
CNF	0	$\checkmark$	0	√	0	0	0	0
PI	$\checkmark$	$\checkmark$	$\checkmark$	$\checkmark$	$\checkmark$	$\checkmark$	0	$\checkmark$
IP	$\checkmark$	$\checkmark$	$\checkmark$	$\checkmark$	$\checkmark$	$\checkmark$	0	$\checkmark$
MODS	$\checkmark$	$\checkmark$	$\checkmark$	$\checkmark$	$\checkmark$	$\checkmark$	$\checkmark$	$\checkmark$

 $\sqrt{}$  : polynomial query

 $\circ$  no polynomial unless P = NP



## Knowledge Compilation Map

 "A Knowledge Compilation Map". Adnan Darwiche, Pierre Marquis. J. Artif. Intell. Res. (JAIR) 17 : 229-264 (2002)

L	CD	FO	SFO	$\wedge \mathbf{C}$	∧BC	٧C	VBC	$\neg C$
NNF	$\checkmark$	0	$\checkmark$	$\checkmark$	$\checkmark$	$\checkmark$	$\checkmark$	√
DNNF	$\checkmark$	<ul> <li>✓</li> </ul>	$\checkmark$	0	0	$\checkmark$	√	0
d-NNF	√	0	$\checkmark$	$\checkmark$	√	$\checkmark$	√	$\checkmark$
s-NNF	$\checkmark$	0	$\checkmark$	$\checkmark$	√	$\checkmark$	√	$\checkmark$
f-NNF	$\checkmark$	0	$\checkmark$	•	•	•	•	$\checkmark$
d-DNNF	$\checkmark$	0	0	0	0	0	0	?
sd-DNNF	$\checkmark$	0	0	0	0	0	0	?
BDD	$\checkmark$	0	$\checkmark$	$\checkmark$	√	$\checkmark$	√	$\checkmark$
FBDD	$\checkmark$	•	0	•	0	•	0	$\checkmark$
OBDD	$\checkmark$	•	$\checkmark$	•	0	•	0	$\checkmark$
OBDD<	$\checkmark$	•	$\checkmark$	٠	- √	•	√	$\checkmark$
DNF	√	<ul> <li>✓</li> </ul>	$\checkmark$	•	√	$\checkmark$	√	•
CNF	√	0	$\checkmark$	$\checkmark$	√	•	√	•
PI	$\checkmark$	$\checkmark$	$\checkmark$	•	•	•	√	•
IP	$\checkmark$	•	•	٠	$\checkmark$	•	•	•
MODS	$\checkmark$	$\checkmark$	$\checkmark$	•	$\checkmark$	•	•	•

 $\sqrt{}$  : polynomial

 $\circ$  no polynomial unless P = NP

no polynomial



## **Our Proposal**

- Forget offline phase
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Conditioning is easy (use assumptions), conjunction also

Forgetting is more difficult to handle

## Some Experiments

- First experiments with benchmarks from ToBDD paper : Too easy !
- Pick some SAT benchmarks from the SAT competition 2011
- Performing 10,000 random queries (Clause Entailment)

Ins	tance		Incr	emental		No Incremental			ToBDD
#V	#C	time	avg	unit lits	conflicts	tim	e avg	conflicts	Time
106,815 17,914 81,184 42,375	552,819 147,360 4,547,858 262,415	2,088 14 1,046 101	0.2 0.001 0.1 0.01	4,652 13,945 8,987 12,744	1,169 85 4,371 3,288	T( 403 T( T(	) 3 0.4 )	> 5,000 4,000 >15,000 >13,000	 10 
42,608	212,741	482	0.04	0	2,805	73	0.7	1,042	-

MUS



### Amortization



Incremental SAT

## Conclusion

- Incremental SAT solving
- CDCL SAT solvers learn from the past
- Keep the solver alive !!
- Possibility to add variables and clauses
- Possibility to remove clauses
- Many potential applications



#### Some references

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